



PSB Singapore

CERTIFICATE

The Certification Body
of TÜV SÜD PSB Pte Ltd

certifies that

STATS CHIPPAC PTE. LTD.

5 Yishun Street 23
Singapore 768442

has established and applies
an Electrostatic Discharge Control Program for

1. Wafer Sort, Wafer Process, Assembly and Test of Integrated Circuit Packages
2. Packages, WLCSP (Wafer Level Chip Scale Packages), Bump and Integrated Passive Device and eWLB (Embedded Wafer Level Ball Grid Array)
3. Packages WLD-CIS (Wafer Level Device – CMOS Image Sensor)

Proof has been furnished that the requirements
according to

ANSI/ESD S20.20 : 2021

(Lead Auditor : Low Teng Leng)

(ESD Program Manager : Mr Roderick Cruz)

are fulfilled. The certificate is valid from **2022-10-29** to **2023-10-30**

Certificate Registration No. **ESD-2018-0017**

Date of Print : **2022-10-29**



SIEW Kwai Heng, Tiffany
Head of Certification Body
Management Systems
Business Assurance Division



◆ CERTIFICAT

◆ CERTIFICADO

◆ СЕРТИФИКАТ

◆ 認證證書

◆ CERTIFICATE

◆ ZERTIFIKAT