



PSB Singapore

CERTIFICATE

The Certification Body
of TÜV SÜD PSB Pte Ltd

certifies that

STATS CHIPAC PTE. LTD.

5 Yishun Street 23
Singapore 768442

has established and applies
an Electrostatic Discharge Control Program for

1. Wafer Sort, Wafer Process, Assembly and Test of Integrated Circuit Packages
2. Packages, WLCSP (Wafer Level Chip Scale Packages), Bump and Integrated Passive Device and eWLB (Embedded Wafer Level Ball Grid Array)
3. Packages WLD-CIS (Wafer Level Device – CMOS Image Sensor)

Proof has been furnished that the requirements
according to

ANSI/ESD S20.20 : 2014

(Lead Auditor : Tiffany Siew Kwai Heng)

(ESD Program Manager : Mr Roderick Cruz)

are fulfilled. The certificate is valid from **2021-10-30** to **2022-10-30**

Certificate Registration No. **ESD-2018-0017**

Date of Print : **2021-11-01**



Clement Teo
Senior Vice President
Business Assurance Division
Management Systems

